

Patent Abstracts of Japan

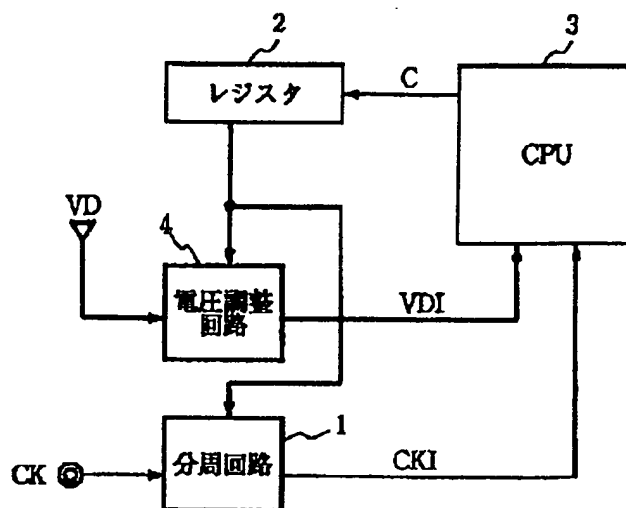
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TITLE : MICROCOMPUTER



ABSTRACT : PURPOSE: To set power supply voltage corresponding to an operation clock frequency and to reduce power consumption with high efficiency by providing a voltage adjustment circuit reducing external power supply voltage and a selection circuit.
 CONSTITUTION: When a high speed operation is not requested from a microcomputer, (1) is written in a register 2 with an output C from a CPU 3. A frequency division circuit 1 divides the frequency of an external clock CK, generate an internal clock CK1 with low frequency and supplies it to the CPU 3. At the same time, a voltage adjustment circuit 4 drops external power supply voltage VD to generate an internal power supply voltage VDI. When the high speed operation is requested from the microcomputer, (0) is written in the register 2 with the output C from the CPU 3. The frequency division circuit 1 uses the external clock CK as the internal clock CK1 and supplies it to the CPU 3. At the same time, the voltage adjustment circuit 4 uses the external power supply voltage VD as the internal power supply voltage VDI and supplies it to the CPU 3.